

Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No.	Application No.:
	02-6037/LSI1P218	Not yet assigned
	Applicant:	
	Aronowitz et al.	
	Filing Date	Group
	Herewith	Not yet assigned

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
A.M	A	U.S. Patent Application Serial No. 10/123,263, filed April 15, 2002
	B	S. M. Sze, Physics of Semiconductor Devices (John Wiley & Sons, New York Ed., 1985) Section 8.6.2
	C	Guha et al., "Compatibility Challenges for High-K Materials Integration into CMOS Technology", MRS Bulletin, March 2002, p. 226-229
Examiner ANH D. MAI		Date Considered 12/22/04

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.